

FIG. 1

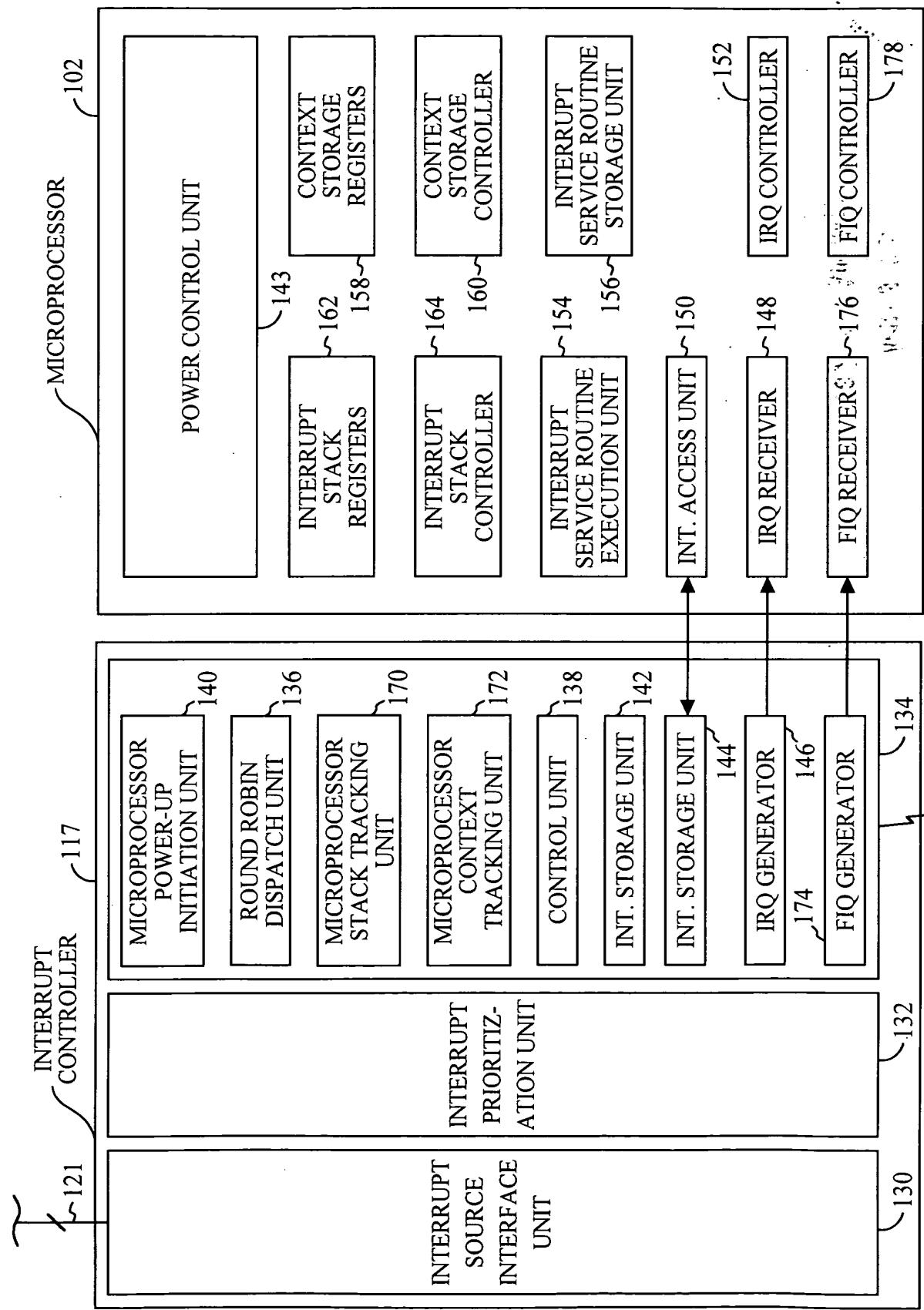


FIG. 2

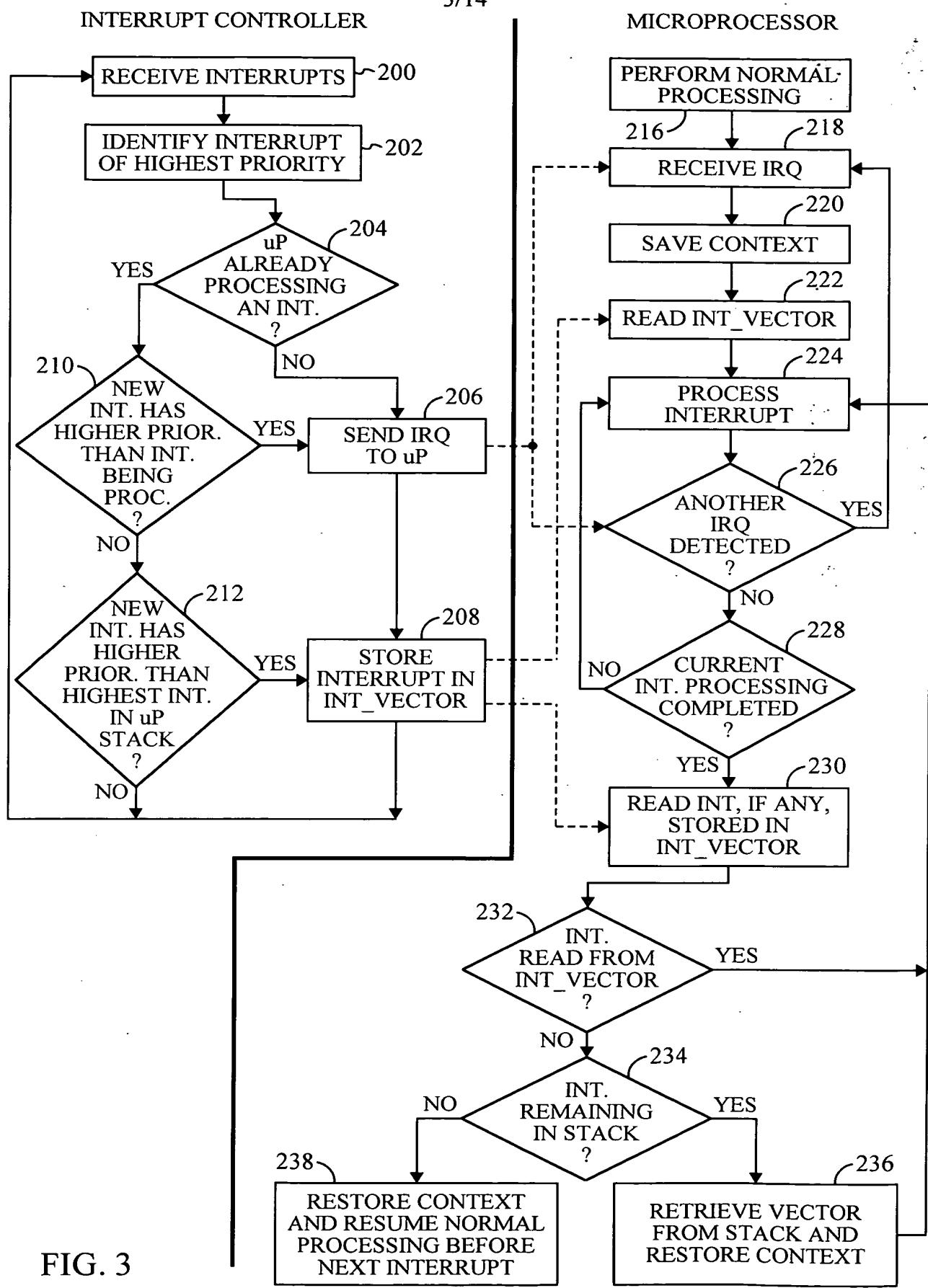


FIG. 3

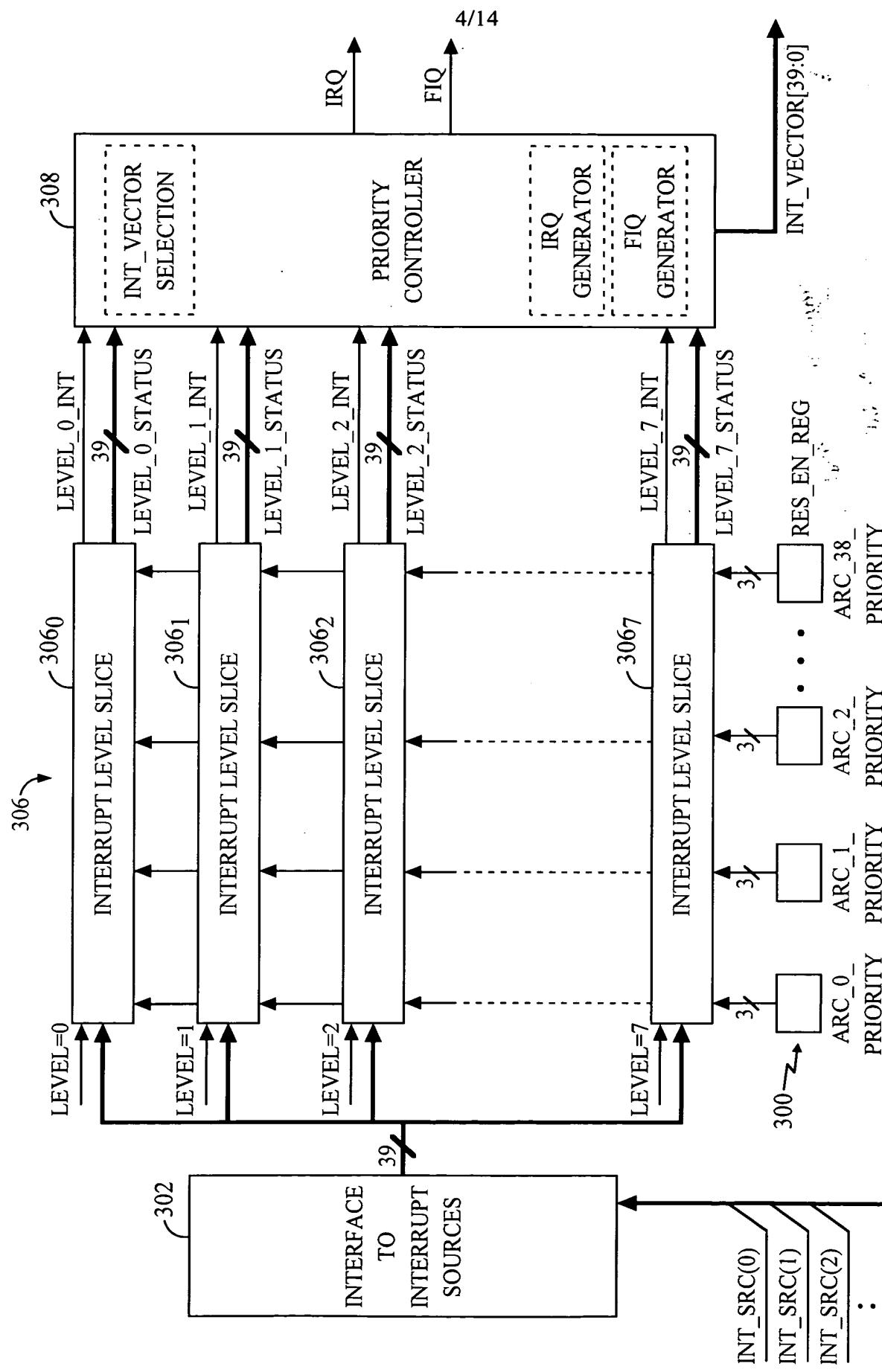


FIG. 4

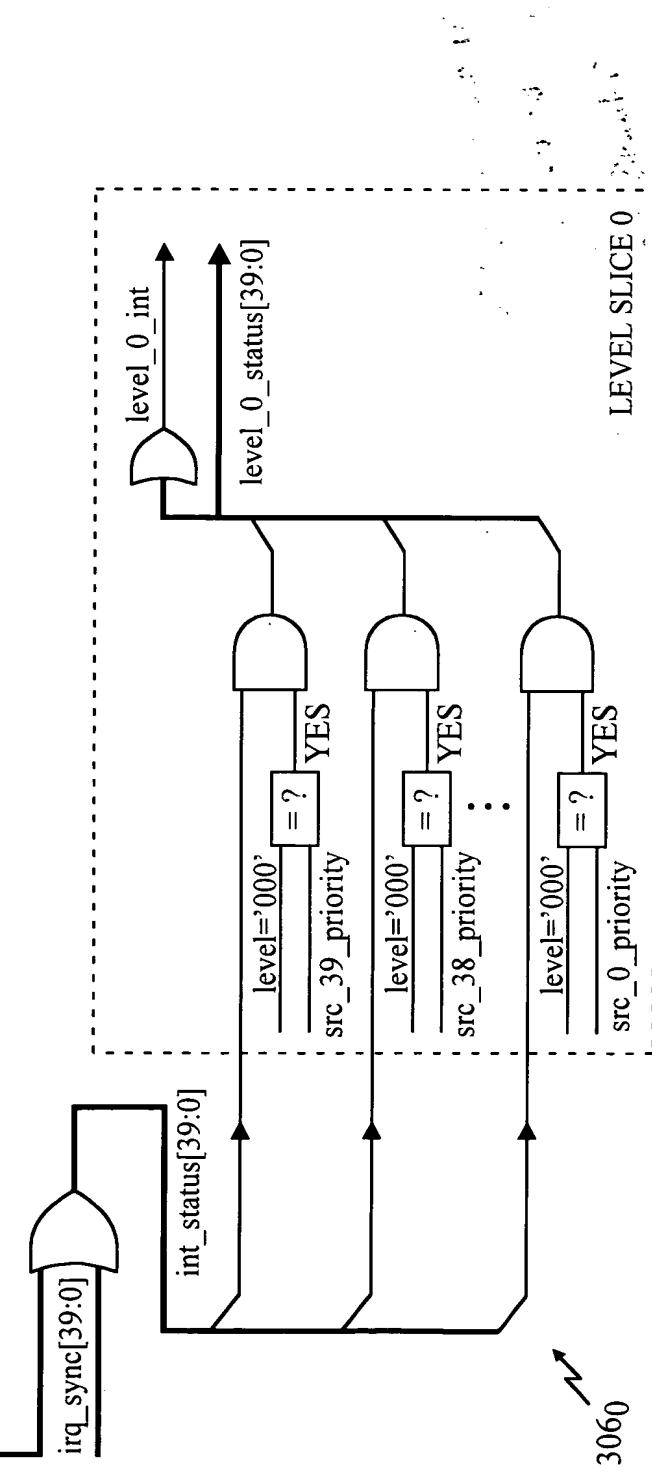
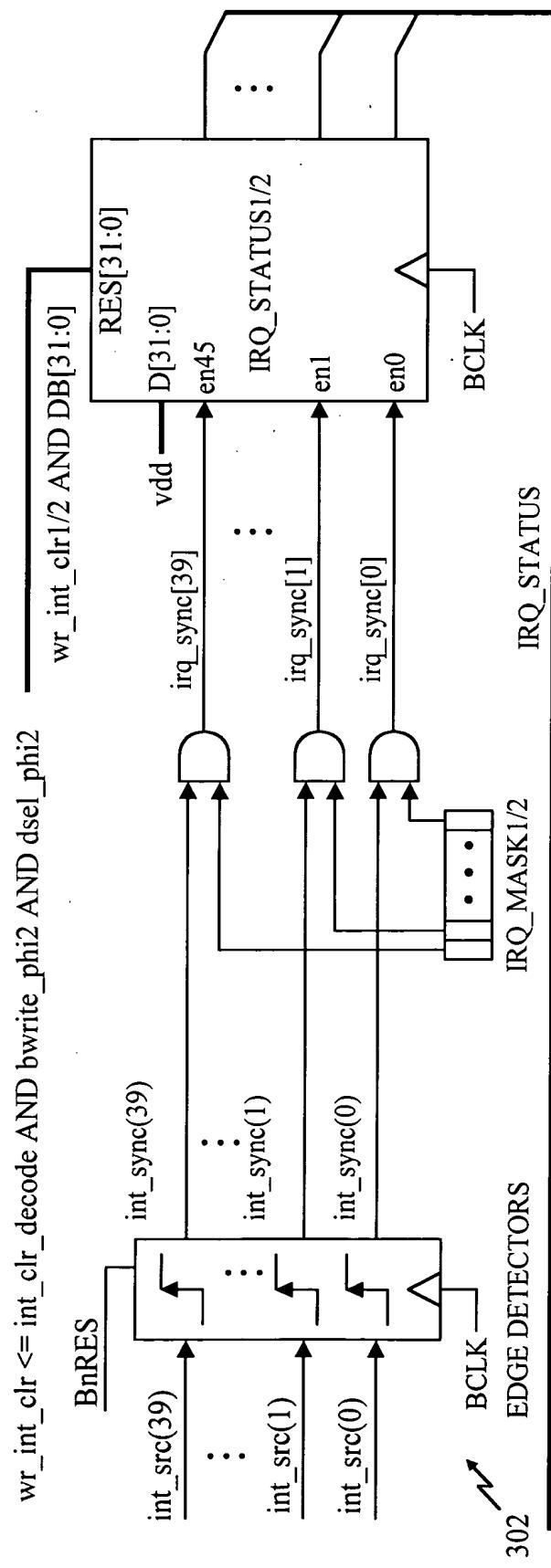


FIG. 5

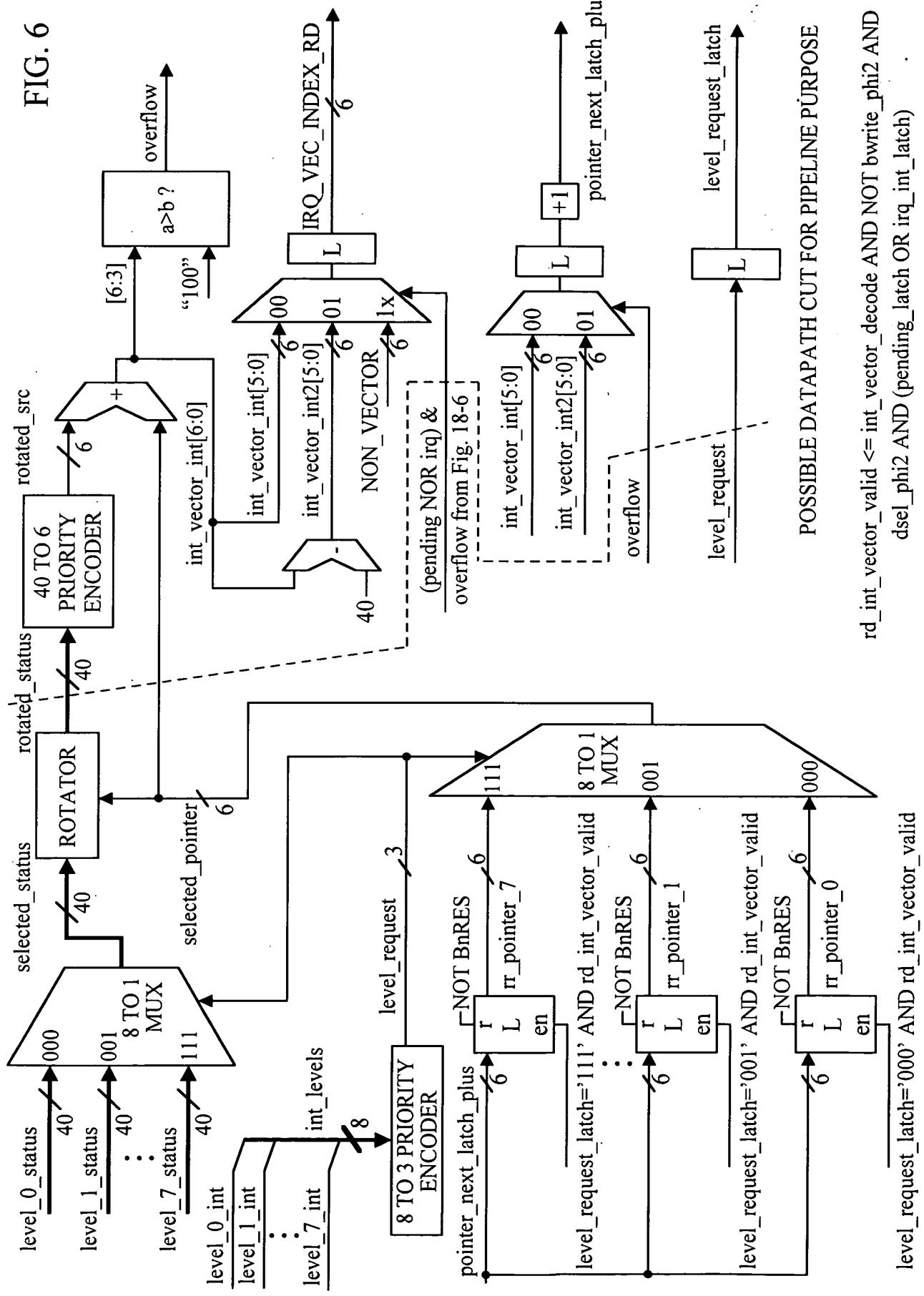
LEVEL SLICE 0

LEVEL SLICE 1

LEVEL SLICE 2

3060

FIG. 6



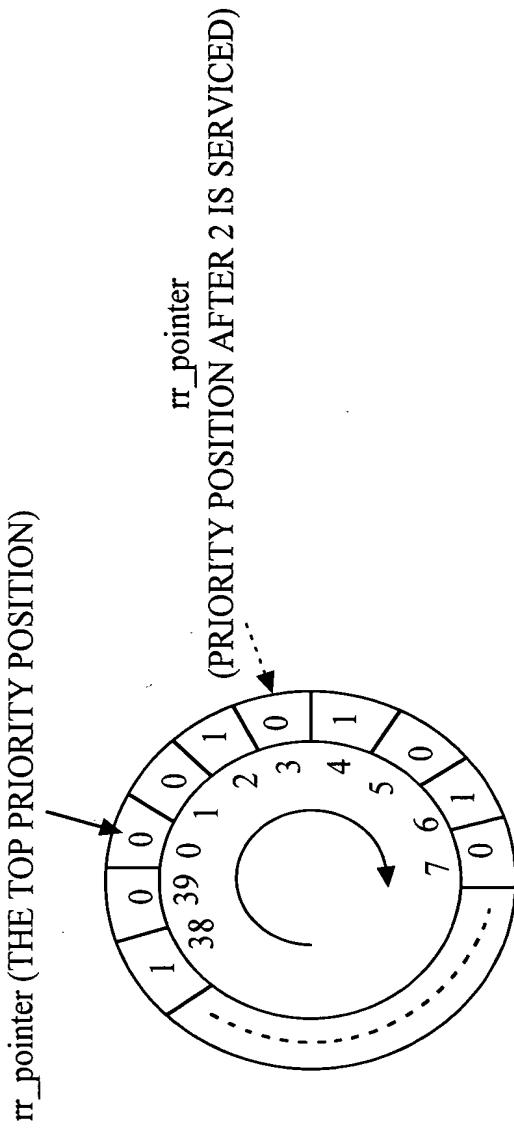
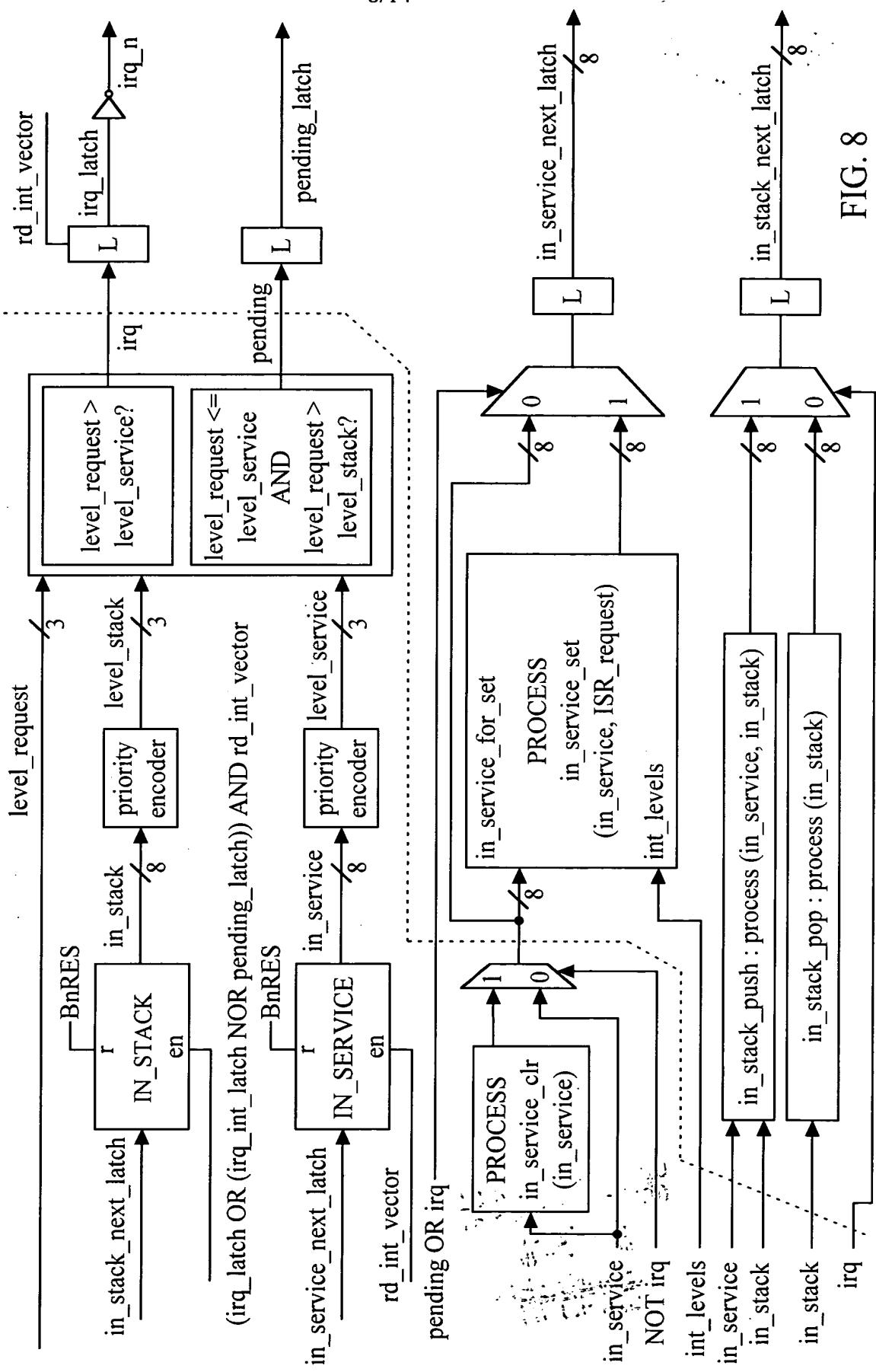


FIG. 7

$(rd_int_vector_phi2)$
 $rd_int_vector \leq int_vector_decode \text{ AND } dsel_phi2 \text{ NOT } bwrite_phi2$

POSSIBLE DATAPATH CUT
FOR PIPELINE PURPOSE



8/14

FIG. 8

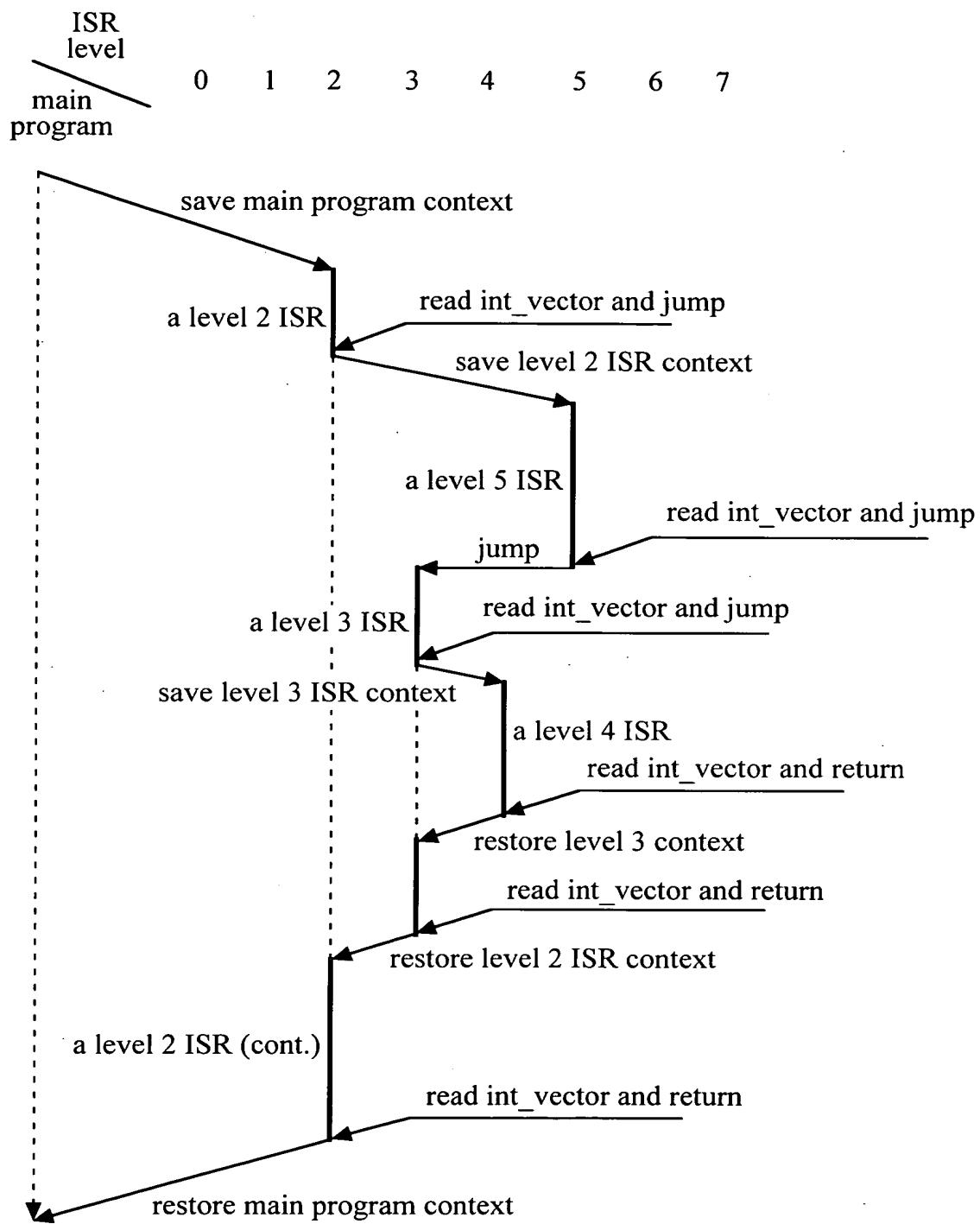


FIG. 9

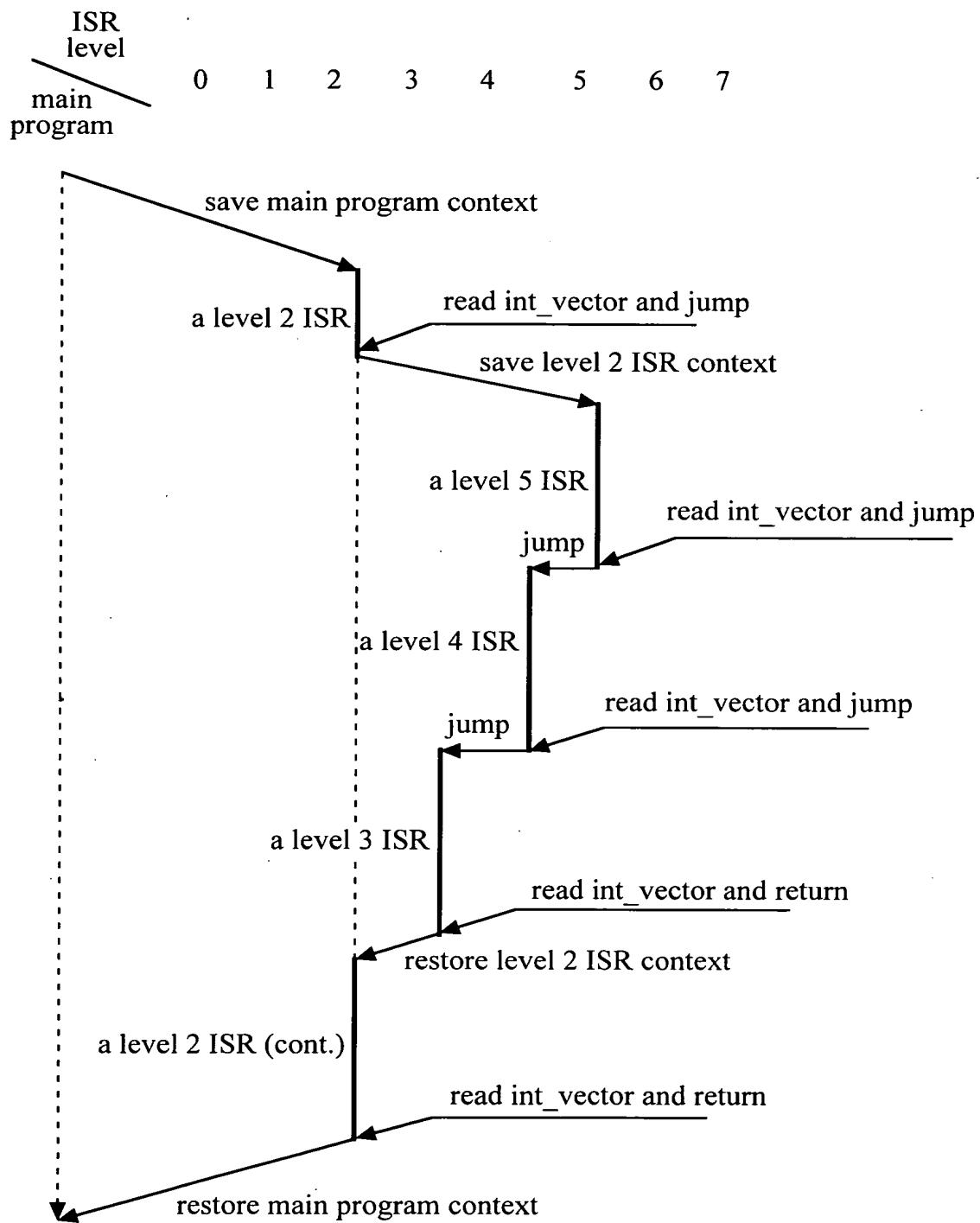


FIG. 10

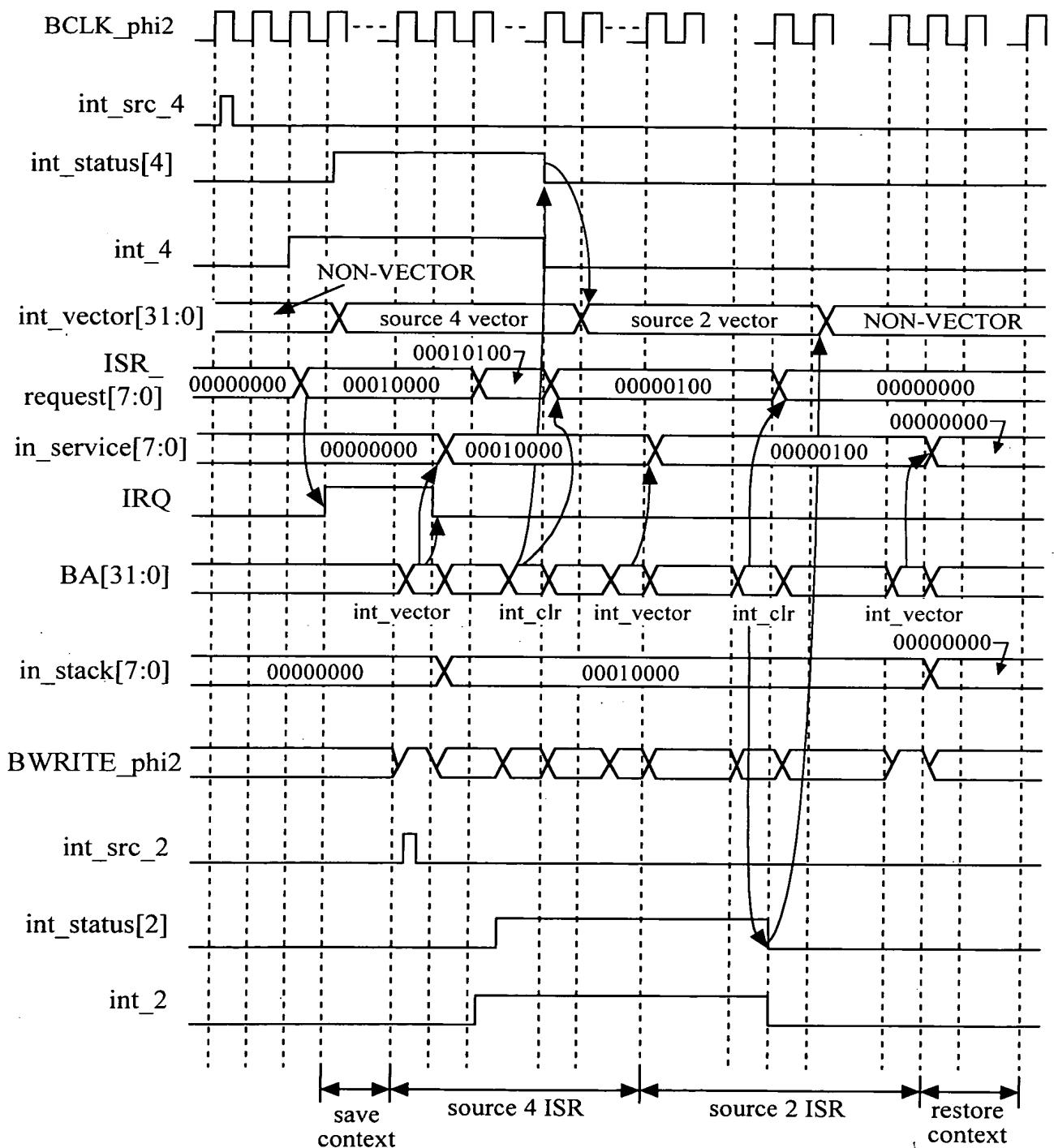


FIG. 11

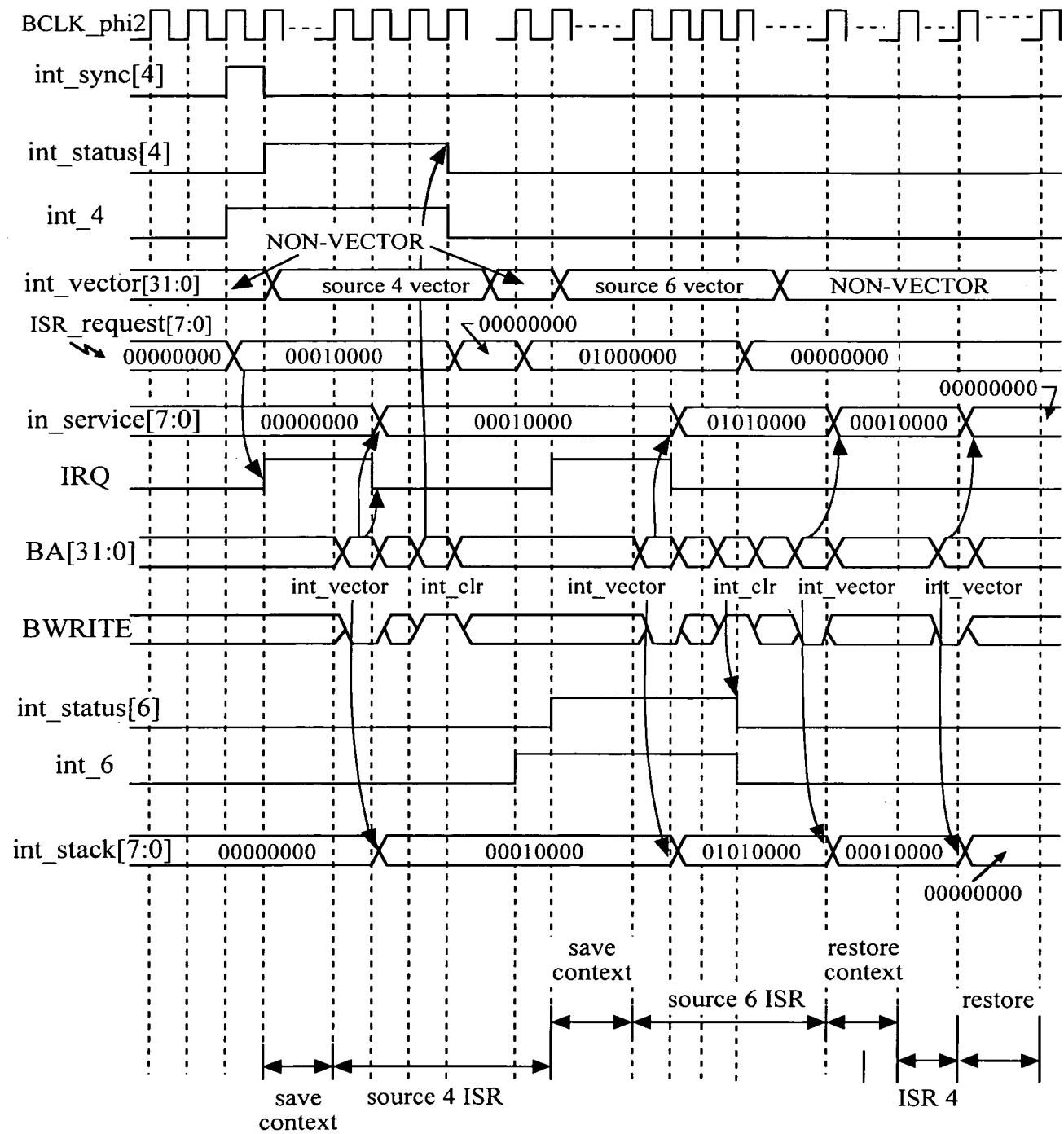


FIG. 12

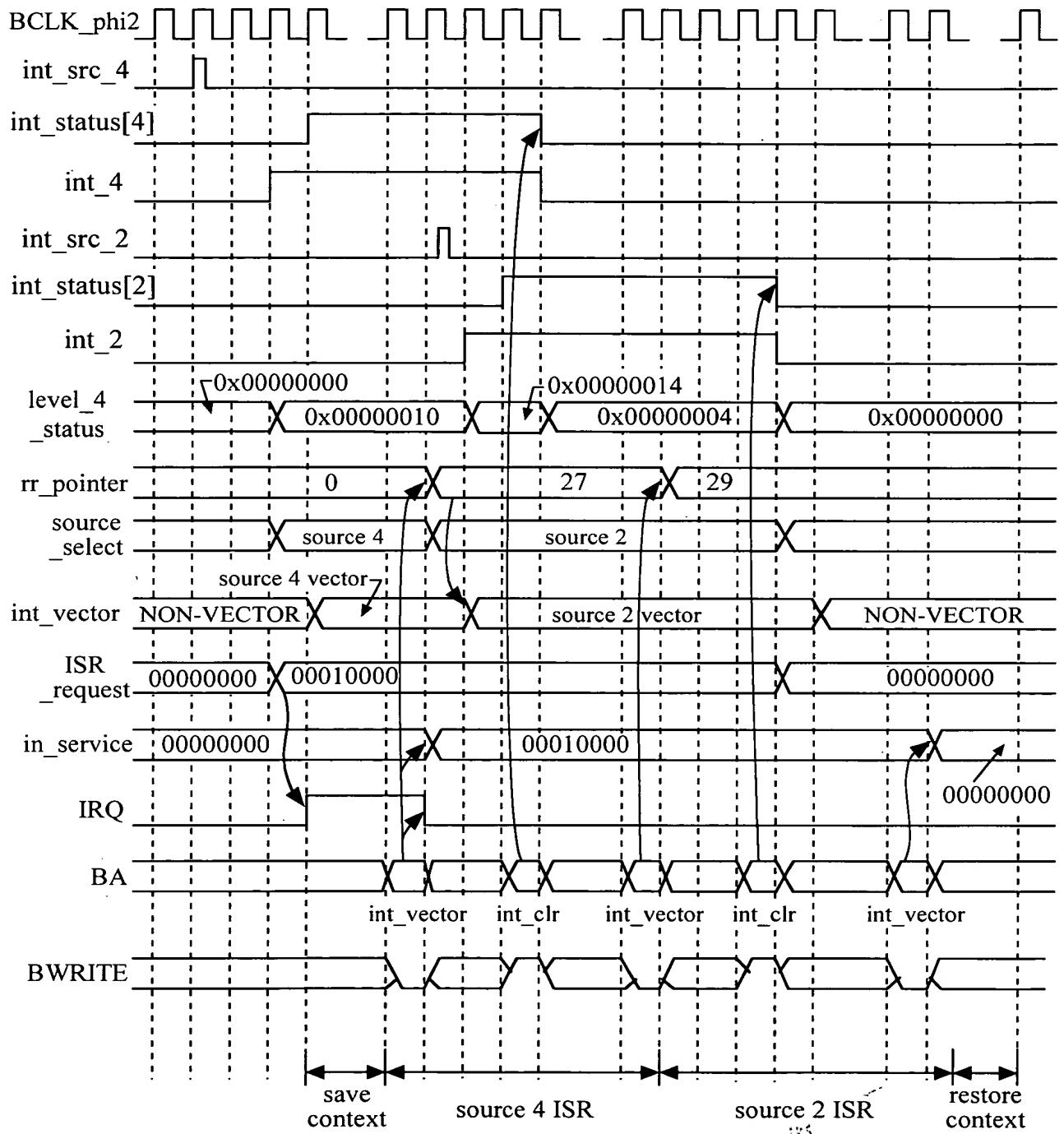


FIG. 13

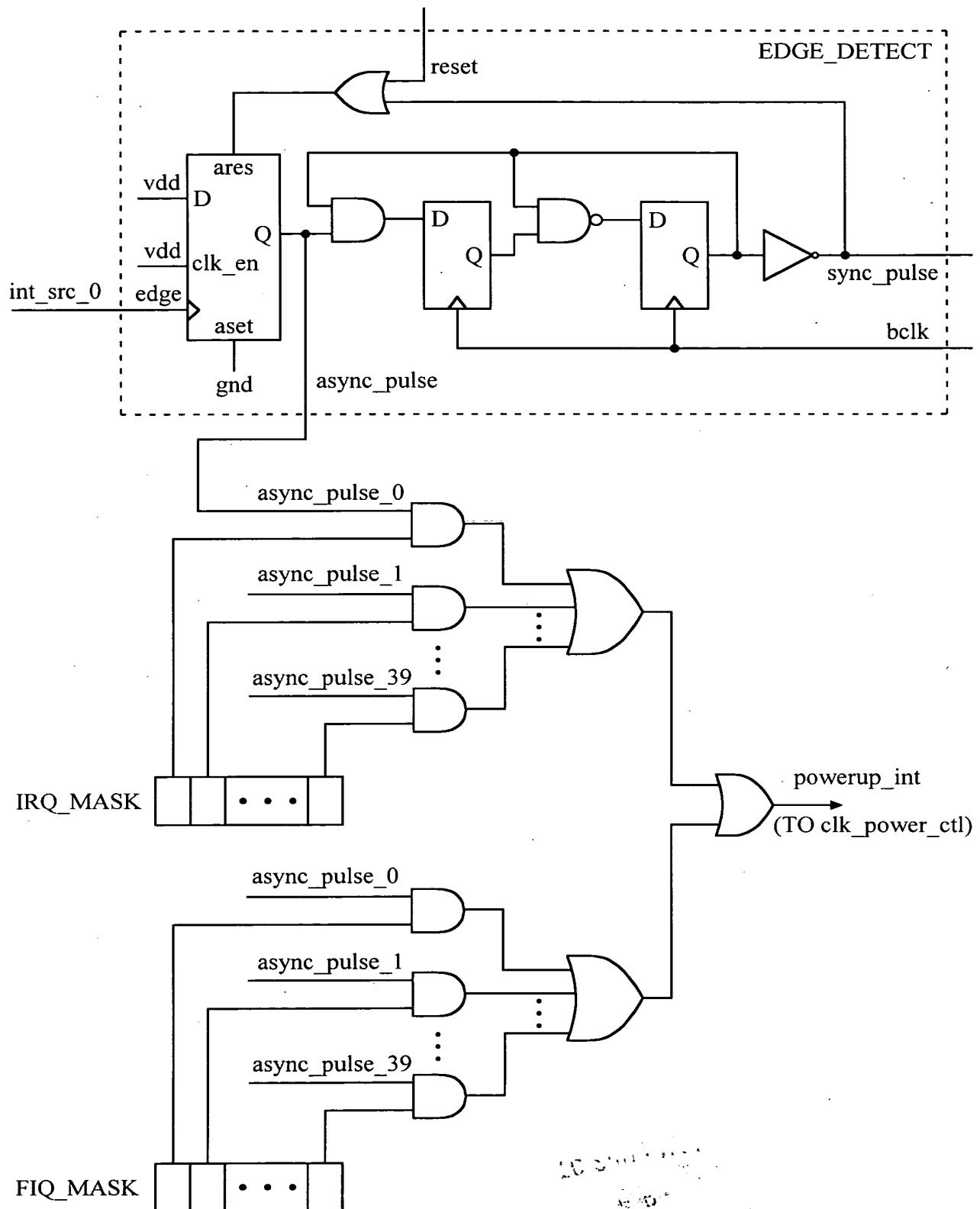


FIG. 14